

OCTOBER 2007

SPECIAL EDITION
TPS EXPRESS NEWSLETTER



**This SPECIAL EDITION of the
NEWSLETTER provides information on the
AN/USM-674(V)3 and the AN/USM-676 .**

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PLANNING

The AN/USM-674(V)3 and the AN/USM-676 are being considered by Program Mangers as an additional tool to work in conjunction with CASS Test Program Sets (TPS).

Bob Augustine (PMA-260D1) is developing plans to utilize this tool. As information becomes available, it will be promulgated to the ATS community by this NEWSLETTER.

BACKGROUND

The following information is provided as background:

The AN/USM-674(V)3 is also known as Huntron ProTrack.

The AN/USM-676 is also known as PinPoint.

Test Routines are analogous to Test Programs

Acceptance is analogous to FAT and TECHEVAL

The following pages contain information about the AN/USM-674(V)3 and the AN/USM-676. Specifically the programming and acceptance documents identified **shall** be used in the development and verification of Test Routines.

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AN/USM-674 diagnostic test station is comprised of the Huntron ProTrack Model 20B and an IBM compatible personal computer to recall, view, and compare previously learned signatures from the response of known good (RFI) CCAs compared to the responses of a suspect faulty CCA. The Protrack uses voltage versus current (VI) diagnostic stimulus with over 8000 combinations of resistance, voltage and frequency Test routine distribution includes comparison signatures, assembly and logistics data on DVDS. The MTR website <https://www.nor.nuwc.navy.mil/mtr> provides detailed information on test routine development, MTR Tracking System, piece parts allowances, training and additional program information.

DEVELOPMENT MANUAL

NAVY(NAVSEA) ST821-AG-PRO-010

AIR FORCE 33D7-38-308-8-1

MARINE CORPS TM 09810A-50/5

REVISION 5

TECHNICAL MANUAL, DEPARTMENT OF DEFENSE, MTR GOLD DISK TEST

ROUTINE DEVELOPMENT REQUIREMENTS MANUAL 0910-LP-708-9200

1 JANUARY 2005

ACCEPTANCE MANUAL

None at this time.

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AN/USM-676



AN/USM-676 PinPoint II test station provides in-circuit functional test of analog and digital components, edge card functional, VI analysis, boundary scan, PXI test and schematic reverse engineering. In-circuit driver/sensor cards execute full functional truth table tests to the whole of the board, selected area (cluster) or device. Fully programmable positive and negative drivers, programmable pull-up and pull-down resistors, no connect pin, true tri-state test, shorts test, high speed functional and voltage tests provide the performance required by test departments. In addition, PinPoint II has been designed to exceed the most stringent military and commercial operating requirements of DEF STD 0053.

DEVELOPMENT MANUAL

Development Requirements for PinPoint,

ACCEPTANCE MANUAL

Acceptance Test Plan for Pin Point II Test Programs, 20 October 2004

Copies of these manuals can be obtained from the Lakehurst web site,
<http://www.lakehurst.navy.mil/nlweb/ecdd-lab/cctars-page2.asp>

NOTE: These Manuals are planned to be updated. They are the what is available today.

If these Manuals are used, as things change we will have a baseline for the change.

If other than these Manuals are used, it will be much more difficult/costly to manage the changes.